

# Abstracts

## A high sensitivity static 2:1 frequency divider up to 19 GHz in 120 nm CMOS

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*H.-D. Wohlmuth, D. Kehrer and W. Simburger. "A high sensitivity static 2:1 frequency divider up to 19 GHz in 120 nm CMOS." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 231-234.*

A completely integrated 19 GHz static 2:1 frequency divider in 120 nm CMOS is presented. The divider operates up to 19 GHz and features an enhanced input sensitivity of 0 dBm over a broad input band of 15 GHz. The circuit draws 44 mA from a single 1.5 V supply. To drive 50  $\Omega$  loads up to 9.5 GHz, an output buffer is also included in the divider circuit.

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